

L Number	Hits	Search Text	DB	Time stamp
1	1971	analog adj communicat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:12
2	28	(analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:28
3	24	((analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))) and analog adj output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:29
4	7	((analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))) and analog adj output and input adj digital	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:30
5	7	((analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))) and analog adj output and input adj digital) and digital adj output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:31
6	6	((analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))) and analog adj output and input adj digital) and digital adj output) and ((digital adj to adj analog) or (DAC))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:32
7	6	((analog adj communicat\$4) and ((isolation adj barrier) or (isolation adj system))) and analog adj output and input adj digital) and digital adj output) and ((digital adj to adj analog) or (DAC)) and ((analog adj to adj digital) or (ADC))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/11/21 11:33

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L16 and transformer	5

Database:

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- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

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USPT	L16 and transformer	5	<u>L32</u>
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USPT	L16 and analog communication	0	<u>L30</u>
USPT	L27 and digital output	1	<u>L29</u>
USPT	L27 and output digital	0	<u>L28</u>
USPT	L22 and analog output	2	<u>L27</u>
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USPT	L22 and analog to digital	0	<u>L25</u>
USPT	L22 and ADC	0	<u>L24</u>
USPT	L22 and DAC	0	<u>L23</u>
USPT	L16 and capacitor	5	<u>L22</u>
USPT	L17 and input digital	0	<u>L21</u>
USPT	L17 and digital to analog	0	<u>L20</u>
USPT	L17 and DAC	0	<u>L19</u>
USPT	L17 and analog output	0	<u>L18</u>
USPT	L2 and capacitor	2	<u>L17</u>
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USPT,PGPB,JPAB,EPAB,DWPI,TDBD	analog isolation barrier	2	<u>L14</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L12 and ADC	6	<u>L13</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L11 and DAC	6	<u>L12</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L9 and digital output	6	<u>L11</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L9 and input digital	0	<u>L10</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L7 and analog output	6	<u>L9</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation circuit)	0	<u>L8</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation system)	6	<u>L7</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation barrier)	0	<u>L6</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation barrier)	0	<u>L5</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation barrier)	0	<u>L4</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation interface)	0	<u>L3</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog communication interface)	12	<u>L2</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	analog communicat\$4	2139	<u>L1</u>